

**VIJAYANAGARA SRI KRISHNADEVARAYA UNIVERSITY**

JNANA SAGAR CAMPUS, VINAYAKA NAGAR, CANTONMENT, BALLARI - 583 105

**REGISTRAR (EVALUATION)****PHONE: 08392-241011**

No: VSKUB/EXAM/PG-TIME TABLE/2017-18/ 2612

DATE: 04.12.2017

**TIME TABLE  
PG EXAMINATIONS, DECEMBER-2017  
{M.Sc. Computer Science I & III Semester}**

Date	I SEM-Subject (Title of the Paper)	III SEM-Subject (Title of the Paper)	Morning Session
16.12.2017 Saturday	-----	Open Elective Papers:	10.00 AM – 1.00 PM
18.12.2017 Monday	75351- HC 1.1 Digital Logic and Computer Design (New) 71605- Digital Logic and Computer Design (Old)	-----	10.00 AM – 1.00 PM
19.12.2017 Tuesday	-----	75371- HC 3.1 Programming in Java (New) 71706 – CS – HC 3.1 Programming in Java (Old)	10.00 AM – 1.00 PM
20.12.2017 Wednesday	75352- HC 1.2 Mathematical Foundation for Computer Science (New) 71606- Mathematical Foundation for Computer Science (Old)	-----	10.00 AM – 1.00 PM
21.12.2017 Thursday	-----	75372- HC 3.2 Data Communication and Computer Networks (New) 71707 – HC 3.2 Data Communication and Computer Networks (Old)	10.00 AM – 1.00 PM
22.12.2017 Friday	75353- HC 1.3 Data Structure Using C++ (New) 71607- Data Structure Using C++ (Old)	-----	10.00 AM – 1.00 PM
23.12.2017 Saturday	-----	75373 – SC 3.3 Computer Graphics New) 71708 – SC 3.3 Computer Graphics (Old)	10.00 AM – 1.00 PM
26.12.2017 Tuesday	75354- SC 1.4 Operating System Principles (New) 71608- Operating System Principles (Old)	-----	10.00 AM – 1.00 PM

*[Handwritten Signature]*  
REGISTRAR (EVALUATION)  
Vijayanagara Sri Krishnadevaraya University,  
Jnana Sagar Campus, Vinayaka Nagar,  
Cantonment, BALLARI-583 104.